INDIAN INSTITUTE OF INTEGRATED SCIENCE AND TECHNOLOGY

An Independent Institute Established by Sri Shirdi Sai Educational Trust (Regd)
Chennai , Tamilnadu, India

RESULT CUM - DETAILED MARKS CARD

Name of the Candidate	VARTHALA CHAITHANYA		Father's Name	V.SUBRAMANYAM
Programme	B Tech Computer Science		D.O.B	28.08.1990
Semester / Year	Consolidate Mark Sheet		Session	2007 to 2011.
Status	Regular		Roll No.	71451k1001

Subject Code	Subject	Marks obtained	Minimum Pass Marks	Maximum Marks
FEN	English	74	50	100
FEM	Engineering Mathematics	73	50	100
FEP	Engineering Physics	85	50	100
FEC	Digital Logic Design	87	50	100
FBE	Programming in C	83	50	100
EVE	Environment Engineering	89	50	100
FED	Engineering Drawing	86	50	100
FIT	Information Technology	87	50	100
FEW	Basic Electronics	72	50	100
ANE	Analog Electronics	70	50	100
CAS	Circuits and Systems	69	50	100
FCS	Foundations of Computer Systems	73	50	100
PUC	Programming using C++	77	50	100
DAS	Data Structures	80	50	100
SOE	Software Engineering	71	50	100
AAD	Algorithm Analysis & Design	62	50	100
MIC	Microprocessors	81	50	100
COG	Computer Graphics	74	50	100
JAP	Java Programming	85	50	100
OPS	Operating Systems	84	50	100
COA	Computer Architecture	89	50	100
LWP	Linux and X-Windows Programming	63	50	100
DMS	Database Management Systems	59	50	100
MIS	Microprocessor Systems	82	50	100
OSE	Object Oriented Software Engineering	64	50	100
CON	Computer Networks	80	50	100
DSP	Digital Signal Processing	72	50	100
DDM	Data Warehousing & Data Mining	79	50	100
MUT	Multimedia Technologies	87	50	100
PRO	Project	59	50	100
TOTAL		2275	1500	3000
DIVISION				First

Date : 25.07.2011

Registrar / Controller of Examination