



INDIAN INSTITUTE OF INTEGRATED SCIENCE AND TECHNOLOGY

An Independent Institute Established by Sri Shirdi Sai Educational Trust (Regd)
Chennai, Tamilnadu, India

RESULT CUM - DETAILED MARKS CARD

Name of the Candidate	TENALI NAVEEN CHAND	Father's Name	TENALI VINOD
Programme	DIPLOMA ECE	D.O.B	24.12.1987
Semester / Year	Consolidated	Session	2005 TO 2008
Status	Fresh	Roll No.	21000111299

Subject Code	Subject	Marks obtained	Minimum Pass Marks	Maximum Marks
ENG	Safety Measures in Industry	58	50	100
MAT	Fundamental of Electricity	65	50	100
BEE	Solders, Flux and Foldering	59	50	100
CYE	Simple Electrical Circuits	63	50	100
PHY	Common Electrical Accessories	70	50	100
MEO	Maintenance Batteries	51	50	100
EGD	Magnetism	66	50	100
COE	Communication Engineering	76	50	100
ELM	Electronic Measurement	73	50	100
DSP	Antenna And Wave Propagation	73	50	100
AWP	Computer Architecture And Org	75	50	100
CRO	Microprocessors And Interfacing	76	50	100
MAI	Digital Signal Processing	78	50	100
MIC	Microprocessors	81	50	100
MAR	Microwave And Radar	76	50	100
CSE	Control Sytems Engg	83	50	100
VLD	Computer Networks	73	50	100
CON	Digital System Design	75	50	100
DSD	Microcontroller	75	50	100
MIC	Vlsi Design	76	50	100
EMS	Embedded System	81	50	100
PRT	Project	74	50	100
TOTAL		432	350	700
DIVISION				First

Entered by

Date : 28.07.2008


Registrar / Controller of Examination