



# INDIAN INSTITUTE OF INTEGRATED SCIENCE AND TECHNOLOGY

An Independent Institute Established by Sri Shirdi Sai Educational Trust (Regd)  
Chennai, Tamilnadu, India

## Mark Statement

Name of the Candidate	AZARUDDIN.A	Father's Name	M.AYOOPKHAN
Programme	BE EEE	D.O.B	17 -01- 1994
Semester / Year	Consolidated	Session	2012 to 2015
Status	Lateral Entry	Roll No.	610E17EEE001

Subject Code	Subject	Marks obtained	Minimum Pass Marks	Maximum Marks
ECT	ELECTRICAL CIRCUIT THEORY	81	50	100
ELM	ELECTRICAL MACHINES-I	74	50	100
EDC	ELECTRONIC DEVICES AND	85	50	100
WOS	CIRCUITS	84	50	100
COA	WORK SHOP PRACTICAL	89	50	100
ELM	COMPUTER APPLICATIONS	87	50	100
MAI	ELECTRICAL MACHINES-II	74	50	100
MIC	POWER SYSTEM	69	50	100
SEM	MICRO CONTROLLER	71	50	100
ADC	SPECIAL ELECTRICAL MACHINES	72	50	100
ECS	ADVANCED COMMUNICATION	66	50	100
CEM	ELECTRICAL CIRCUITS SIMULATION	71	50	100
PLC	CONTROL OF ELECTRICAL MACHINES	72	50	100
MIP	PROGRAMMABLE LOGIC CONTROLLER	66	50	100
FFA	ARTIFICIAL NEURAL NETWORKS	72	50	100
FUS	ADVANCED CONTROL THEORY	64	50	100
BUC	POWER GENERATION SYSTEMS	70	50	100
PBE	MODERN POWER SYSTEM ANALYSIS	71	50	100
BMS	HIGH VOLTAGE ENGINEERING	72	50	100
BUE	SPECIAL ELECTRICAL MACHINES	76	50	100
COL	PROJECT	63	50	100
TOTAL		1600	1050	2100
DIVISION				

Entered by

First

Date : 25.06.2015

  
Registrar / Controller of Examination