



INDIAN INSTITUTE OF INTEGRATED SCIENCE AND TECHNOLOGY

An Independent Institute Established by Sri Shirdi Sai Educational Trust (Regd)
Chennai, Tamilnadu, India

RESULT CUM - DETAILED MARKS CARD

Name of the Candidate	GOPI.J	Father's Name	JAYARAM
Programme	Bachelor of Ele. and Elec. Engg.	D.O.B	06/06/1982
Semester / Year	Consolidated	Session	2014-2017
Status	Lateral Entry	Roll No.	3216060112

Subject Code	Subject	Marks obtained	Minimum Pass Marks	Maximum Marks
MAT	Mathematics	82	50	100
EVS	Environmental Science & Engg.	64	50	100
ELT	Electromagnetic Theory	80	50	100
ELM	Electrical Machines	72	50	100
ELD	Electronic Devices	79	50	100
ATH	Applied Thermodynamics	87	50	100
NUM	Numerical Methods	76	50	100
ELM	Electrical Machines-II	70	50	100
TRD	Transmission & Distribution	80	50	100
COS	Control Systems	72	50	100
ELC	Electronic Circuits	56	50	100
OOP	Object Oriented Programming	69	50	100
POE	Power Electronics	56	50	100
PAS	Protection & Switchgear	69	50	100
COE	Communication Engineering	72	50	100
DLC	Digital Logic Circuits	59	50	100
LIC	Linear Integrated Circuits	83	50	100
PEH	Professional Ethics & Human.	57	50	100
FIM	Solid State Drives	81	50	100
BUE	Power System Analysis	61	50	100
MAE	Measurements & Instrument	59	50	100
ORB	Digital Signal Processing	81	50	100
HRM	Microprocessor & Controllers	67	50	100
QUT	Principles of Managements	73	50	100
PSO	Power System Operations	70	50	100
HVE	High Voltage Engineering	69	50	100
DEA	Design of Electrical Apparatus	73	50	100
TQM	Total Quality Management	77	50	100
PRO	Project	70	50	100
TOTAL		2064	1450	2900
DIVISION				First

Date: 26.06.2017

Registrar / Controller of Examination