



# INDIAN INSTITUTE OF INTEGRATED SCIENCE AND TECHNOLOGY

An Independent Institute Established by Sri Shirdi Sai Educational Trust (Regd)  
Chennai, Tamilnadu, India

## RESULT CUM - DETAILED MARKS CARD

|                       |               |               |               |
|-----------------------|---------------|---------------|---------------|
| Name of the Candidate | G.PURNA ANAND | Father's Name | G.GUNASEKARAN |
| Programme             | DIPLOMA ECE   | D.O.B         | 14/05/1990    |
| Semester / Year       | CONSOLIDATED  | Session       | 2011 TO 2014  |
| Status                | FRESH         | Roll No.      | 71451K1011    |

| Subject Code | Subject                         | Marks obtained | Minimum Pass Marks | Maximum Marks |
|--------------|---------------------------------|----------------|--------------------|---------------|
| COE          | Communication Engineering       | 76             | 50                 | 100           |
| ELM          | Electronic Measurement          | 73             | 50                 | 100           |
| DSP          | Antenna And Wave Propagation    | 73             | 50                 | 100           |
| AWP          | Computer Architecture And Org   | 75             | 50                 | 100           |
| CRO          | Microprocessors And Interfacing | 76             | 50                 | 100           |
| MAI          | Digital Signal Processing       | 78             | 50                 | 100           |
| MIC          | Microprocessors                 | 81             | 50                 | 100           |
| MAR          | Microwave And Radar             | 76             | 50                 | 100           |
| CSE          | Control Sytems Engg             | 83             | 50                 | 100           |
| VLD          | Computer Networks               | 73             | 50                 | 100           |
| CON          | Digital System Design           | 75             | 50                 | 100           |
| DSD          | Microcontroller                 | 75             | 50                 | 100           |
| MIC          | Vlsi Design                     | 76             | 50                 | 100           |
| EMS          | Embedded System                 | 81             | 50                 | 100           |
| PRT          | Project                         | 74             | 50                 | 100           |
| TOTAL        |                                 | 1145           | 750                | 1500          |
| DIVISION     |                                 |                |                    | First         |

Entered by

Date: 28.07.2014

  
Registrar / Controller of Examination