



INDIAN INSTITUTE OF INTEGRATED SCIENCE AND TECHNOLOGY

An Independent Institute Established by Sri Shirdi Sai Educational Trust (Regd)
Chennai, Tamilnadu, India

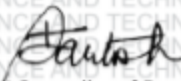
RESULT CUM - CONSOLIDATED MARKS CARD

Name of the Candidate	ARJUNA SALAGAR	Father's Name	GOVINDAPPA
Programme	Diploma In EEE	D.O.B	22.07.1993
Semester / Year	Consolidated	Session	2012 to 2014
Status	Lateral Entry	Roll No.	610D14EE001

Subject Code	Subject	Marks obtained	Minimum Pass Marks	Maximum Marks
MAS	Electrical Circuit Theory	70	50	100
MED	Electrical Machines-I	69	50	100
APM	Electronic Devices and Circuits	73	50	100
POM	Work Shop Practical	77	50	100
MEA	Computer Applications	80	50	100
CAM	Electrical Machines-II	76	50	100
COA	Measurements and Instruments	79	50	100
FLM	Power System	65	50	100
TMD	Micro Controller	81	50	100
IEP	Special Electrical Machines	74	50	100
AUE	Advanced Communication	85	50	100
PRM	Electrical Circuits Simulation	84	50	100
INM	Control of Electrical Machines	89	50	100
TOM	Programmable Logic Controller	87	50	100
MAD	Project	74	50	100
TOTAL		1163	750	1500
DIVISION				First

Entered by

Date: 29.07.2014


Registrar / Controller of Examination