



INDIAN INSTITUTE OF INTEGRATED SCIENCE AND TECHNOLOGY

An Independent Institute Established by Sri Shirdi Sai Educational Trust (Regd)
Chennai, Tamilnadu, India

RESULT CUM - CONSOLIDATED MARKS CARD

Name of the Candidate	G. RAMESH	Father's Name	R.V. GOVINDARAJALU
Programme	BSC ELECTRONICS	D.O.B	21-05-1972
Semester / Year	-----	Session	1999 - 2002
Status	Fresh	Roll No.	4101600111

Subject Code	Subject	Marks obtained	Minimum Pass Marks	Maximum Marks
HSE	TECHNICAL ENGLISH	76	50	100
MAT	MATHEMATICS	74	50	100
PHY	PHYSICS	77	50	100
CYE	CHEMISTRY	61	50	100
GEC	COMPUTERPROGRAMMING	69	50	100
GEE	DIGITAL SYSTEM DESIGN	60	50	100
GEB	BASIC ELECTRICAL AND ELECTRONICS	79	50	100
SIS	SIGNALS & SYSTEMS	75	50	100
ANE	ANALOG ELECTRONICS	77	50	100
GEP	GENERAL PROFICIENCY	69	50	100
DIE	DIGITAL ELECTRONICS	59	50	100
COS	COMMUNICATION SYSTEMS	70	50	100
EMT	ELECTRO MAGNETIC THEORY	73	50	100
THD	THERMODYNAMICS	76	50	100
COE	COMPUTER NETWORKS	74	50	100
ELM	ELECTRONIC MEASUREMENT	66	50	100
CSE	CONTROL SYTEMS	63	50	100
AWP	ANTENNA AND WAVE PROPAGATION	65	50	100
CRO	COMPUTER ARCHITECTURE AND ORG	68	50	100
MAI	MICROPROCESSORS AND INTERFACING	57	50	100
MIC	MICROPROCESSORS	72	50	100
TOTAL		1460	1050	2100
DIVISION				First

Date: 29.07.2002


Registrar / Controller of Examination