



# INDIAN INSTITUTE OF INTEGRATED SCIENCE AND TECHNOLOGY

An Independent Institute Established by Sri Shirdi Sai Educational Trust (Regd)  
Chennai, Tamilnadu, India

## RESULT CUM - DETAILED MARKS CARD

Name of the Candidate	NASARDEEN.A	Father's Name	AMEER.E
Programme	B TECH EEE (LATERAL ENTRY)	D.O.B	30.07.1988
Semester / Year	Consolidated Mark Sheet	Session	2013-2016
Status	Distance Education	Roll No.	71AN16BEE006

Subject Code	Subject	Marks obtained	Minimum Pass Marks	Maximum Marks
ECT	ELECTRICAL CIRCUIT THEORY	70	50	100
ELM	ELECTRICAL MACHINES-I	69	50	100
EDC	ELECTRONIC DEVICES AND CIRCUITS	73	50	100
WOS	WORK SHOP PRACTICAL	77	50	100
COA	COMPUTER APPLICATIONS	80	50	100
ELM	ELECTRICAL MACHINES-II	76	50	100
MIC	POWER SYSTEM	69	50	100
SEM	MICRO CONTROLLER	71	50	100
ADC	SPECIAL ELECTRICAL MACHINES	72	50	100
ECS	ADVANCED COMMUNICATION	66	50	100
CEM	ELECTRICAL CIRCUITS SIMULATION	71	50	100
PLC	CONTROL OF ELECTRICAL MACHINES	72	50	100
MIP	PROGRAMMABLE LOGIC CONTROLLER	66	50	100
FFA	ARTIFICIAL NEURAL NETWORKS	72	50	100
FUS	ADVANCED CONTROL THEORY	64	50	100
BUC	POWER GENERATION SYSTEMS	70	50	100
PBE	MODERN POWER SYSTEM ANALYSIS	71	50	100
BMS	HIGH VOLTAGE ENGINEERING	72	50	100
BUE	SPECIAL ELECTRICAL MACHINES	76	50	100
COL	PROJECT	63	50	100
TOTAL		1420	1050	2100
DIVISION				First

DATE : 28.07.2016

  
CONTROLLER OF EXAMINATIONS