



INDIAN INSTITUTE OF INTEGRATED SCIENCE AND TECHNOLOGY


An Independent Institute Established by Sri Shirdi Sai Educational Trust (Regd)
Chennai, Tamilnadu, India

RESULT CUM - DETAILED MARKS CARD

Name of the Candidate	ADURTHI SURESH	Father's Name	ADURTHI VENKATARAO
Programme	BE (Electrical and Electronics Engineering)	D.O.B	05.06.1991
Semester / Year	CONSOLIDATED	Session	2010-2014
Status	Regular	Roll No.	KA14BEEEE2014

Subject Code	Subject	Marks obtained	Minimum Pass Marks	Maximum Marks
TEE	TECHNICAL ENGLISH	74	50	100
MAT	MATHEMATICS	71	50	100
ENC	ENGINEERING CHEMISTRY	78	50	100
CYP	COMPUTER PROGRAMMING	81	50	100
EGR	ENGINEERING GRAPHICS	83	50	100
ENP	ENGINEERING PHYSICS	72	50	100
BEE	BASIC ELE. AND ELECTRONICS	72	50	100
ECT	ELECTRICAL CIRCUIT THEORY	70	50	100
ELM	ELECTRICAL MACHINES-I	69	50	100
EDC	ELECTRONIC DEVICES AND CIRCUITS	73	50	100
WOS	WORK SHOP PRACTICAL	77	50	100
COA	COMPUTER APPLICATIONS	80	50	100
ELM	ELECTRICAL MACHINES-II	76	50	100
PWS	POWER SYSTEM	69	50	100
MIC	MICRO CONTROLLER	71	50	100
SEM	SPECIAL ELECTRICAL MACHINES	72	50	100
ACM	ADVANCED COMMUNICATION	66	50	100
ECS	ELECTRICAL CIRCUITS SIMULATION	71	50	100
CEM	CONTROL OF ELECTRICAL MACHINES	72	50	100
PLC	PROGRAMMABLE LOGIC CONTROLLER	66	50	100
ANS	ARTIFICIAL NEURAL NETWORKS	72	50	100
ACT	ADVANCED CONTROL THEORY	64	50	100
PGS	POWER GENERATION SYSTEMS	70	50	100
MPS	MODERN POWER SYSTEM ANALYSIS	71	50	100
HVE	HIGH VOLTAGE ENGINEERING	72	50	100
SEM	SPECIAL ELECTRICAL MACHINES	76	50	100
PRO	PROJECT	63	50	100
TOTAL		1951	1400	2800
DIVISION				First

Date: 28.07.2014


Controller of Examination