



INDIAN INSTITUTE OF INTEGRATED SCIENCE AND TECHNOLOGY

An Independent Institute Established by Sri Shirdi Sai Educational Trust (Regd)
Chennai, Tamilnadu, India

RESULT CUM - CONSOLIDATED MARKS CARD

Name of the Candidate	M.VINAYAGAM	Father's Name	S.MANICKAM
Programme	Diploma In EEE	D.O.B	02.05.1982
Semester / Year	Consolidated	Session	2012 to 2014
Status	Lateral Entry	Roll No.	610D14EE002

Subject Code	Subject	Marks obtained	Minimum Pass Marks	Maximum Marks
MAS	Electrical Circuit Theory	81	50	100
MED	Electrical Machines-I	74	50	100
APM	Electronic Devices and Circuits	85	50	100
POM	Work Shop Practical	84	50	100
MEA	Computer Applications	89	50	100
CAM	Electrical Machines-II	87	50	100
COA	Measurements and Instruments	74	50	100
FLM	Power System	61	50	100
TMD	Micro Controller	75	50	100
IEP	Special Electrical Machines	60	50	100
AUE	Advanced Communication	64	50	100
PRM	Electrical Circuits Simulation	82	50	100
INM	Control of Electrical Machines	79	50	100
TOM	Programmable Logic Controller	81	50	100
MAD	Project	69	50	100
TOTAL		1145	750	1500
DIVISION				First

Entered by

Date : 20.07.2014


Registrar / Controller of Examination

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