



INDIAN INSTITUTE OF INTEGRATED SCIENCE AND TECHNOLOGY

An Independent Institute Established by Sri Shirdi Sai Educational Trust (Regd)
Chennai, Tamilnadu, India

RESULT CUM - DETAILED MARKS CARD

Name of the Candidate	KORESH NAYAGAM,V	Father's Name	VELLADURA
Programme	Diploma In EEE	D.O.B	25.04.1994
Semester / Year	Consolidated	Session	2010-2013
Status	Regular	Roll No.	12658111486

Subject Code	Subject	Marks obtained	Minimum Pass Marks	Maximum Marks
ENG	English	70	50	100
ENM	Engineering Mathematics	69	50	100
ENP	Engineering Chemistry	73	50	100
ENC	Basic Electrical Engineering	77	50	100
BEE	Mechanical workshop	80	50	100
MEW	Engineering Physics	76	50	100
END	Engineering Drawing	79	50	100
INT	Information Technology	65	50	100
MAS	Electrical Circuit Theory	81	50	100
MED	Electrical Machines-I	74	50	100
APM	Electronic Devices and Circuits	85	50	100
POM	Work Shop Practical	84	50	100
MEA	Computer Applications	89	50	100
CAM	Electrical Machines-II	87	50	100
COA	Measurements and Instruments	74	50	100
FLM	Power System	61	50	100
TMD	Micro Controller	75	50	100
JEP	Special Electrical Machines	60	50	100
AUE	Advanced Communication	64	50	100
PRM	Electrical Circuits Simulation	82	50	100
INM	Control of Electrical Machines	79	50	100
TOM	Programmable Logic Controller	81	50	100
MAD	Project	69	50	100
TOTAL		1734	1150	2300
DIVISION				First

Entered by

Date 27.07.2013

Registrar / Controller of Examination