



INDIAN INSTITUTE OF INTEGRATED SCIENCE AND TECHNOLOGY

An Independent Institute Established by Sri Shirdi Sai Educational Trust (Regd)
Chennai, Tamilnadu, India

RESULT CUM - DETAILED MARKS CARD

Name of the Candidate	HARIHARAN.V	Father's Name	VIJAYAKUMAR.S
Programme	BACHELOR OF ELECTRONICS AND INSTRUMENTATION ENGINEERING	D.O.B	01.04.1994
Semester / Year	First Year	Session	2011-2015
Status	Regular	Roll No.	1530BEEI2015

Subject Code	Subject	Marks obtained	Minimum Pass Marks	Maximum Marks
TEE	TECHNICAL ENGLISH	74	50	100
MAT	MATHEMATICS	71	50	100
ENC	ENGINEERING CHEMISTRY	78	50	100
CMP	COMPUTER PROGRAMMING	81	50	100
ENG	ENGINEERING GRAPHICS	83	50	100
ENP	ENGINEERING PHYSICS	72	50	100
BEE	BASIC ELE. AND ELECTRONICS ENGINEERING	72	50	100
TBV	TRANSFORMS & BOUNDARY VALUE PROBLEMS SOLVING	70	50	100
PRS	ANALYSIS OF ELECTRIC CIRCUITS	69	50	100
AEC	PRINCIPLES OF ENVI. SCIENCE	73	50	100
PES	BASIC ELECTRONICS ENGINEERING	77	50	100
BEE	SIGNALS AND SYSTEMS	80	50	100
SAS	ELECTROMAGNETIC THEORY	76	50	100
ELT	ELECTRONICS DEVICES AND CIRCUITS	79	50	100
EDC	POWER ELECTRONICS	69	50	100
POE	DESIGN OF ELECTRICAL APPARATUS	71	50	100
DEA	INDUSTRIAL INSTRUMENTATION	72	50	100
INI	MICROPRO. AND MICROCONT.	66	50	100
MAM	POWER SYSTEM ANALYSIS	71	50	100
PSA	POWER ELECTRONICS AND DRIVES	72	50	100
PED	LOGIC AND DISTRIBUTED CONTROL	66	50	100
LDC	ARTIFICIAL NEURAL NETWORKS	72	50	100
ANN	DIGITAL IMAGE PROCESSING	64	50	100
DIP	POWER GENERATION SYSTEMS	70	50	100
PGS	ROBOTICS AND AUTOMATION	71	50	100
RAA	HIGH VOLTAGE ENGINEERING	72	50	100
HVE	SPECIAL ELECTRICAL MACHINES	76	50	100
SEM	PROJECT	63	50	100
PRO	TOTAL	2114	1450	2900
DIVISION				First

Entered by

Date 28.07.2015

Registrar / Controller of Examination