Rules

1. A candidate shall be declared passed in the examination if he/she secued at least 40% marks in internal assignment and external examination separately and 50% marks in aggregate.

2. A candidate is required to pass in theory as well as practical paper separately. A candidate who secure below 40% marks in internal or external examination in a paper. A candidate who secured 40% and above but below 50% marks (internal + external) in a paper will be declared failed.

3. A candidate who secure below 50% marks will be given D Grade and considered failed; a candidate who secured overall 50% marks and above but below 60% marks will be awarded grade C; a candidate who secures overall 60% marks and above but below 75% marks will be awarded grade B; a candidate who secures overall 75% marks and above marks will be awarded grade A in the mark sheet issued at final semester / year.

40% and above but below 50%	GRADE D	POOR
50% and above but below 60%	GRADE C	GOOD
60% and above but below 75%	GRADE B	VERY GOOD
75% and above	GRADE A	EXCELLENT

4. Despite the candidate being declared failed, he will be eligible to appear in the next semester/year examination. Due to paper will be treated as backlog paper (s). To clear the due paper (s) the candidate will be allowed to appear in the backlog paper (s) along with the next semester/yearly examination. If the candidate does not avail these opportunities or fails in these atempts his/her final semester/year result will be declared but mark sheet will not be issued.

5, No grace marks shall be awarded in internal assignments, practical paper & projects. The grace marks will be awarded only on aggregate marks of the theory paper upto 1% of the maximum of the two theory papers. No grace marks will be awarded in the due papers.

6. A candidate shall not be given any chance to improve the division/marks in any paper(s) in any semester/year.

7. Re-evaluation of answer book shall not be permissible. However, re-totaling is permitted as institutions rules.

8. In case of any mistake being detected in the preparation of mark sheet and brought to notice aferwards, the insstitutions will be fully empowered to correct the same.

9. The institution has the right to change any rule without any prior informamation.

10. The final semester/year mark sheet will show total marks of all the semester(s) of that course as well as whether the candidates has cleared all the semester(s) year or not.

11. Lateral Entry (LE) The Institution allows exemption regards to the duration and the numbers of paper of the course under Later Entry Scheme.

12. Candidate registered under Institution are of two categories i.e., Regular and OES. The candidate who is registered and studying regularly at the study centers come under regular category and candidate who are registered under open Education system come under OES category.

13. The candidate who is appearing at the examination or the fisst time (first atempt) is considered as fresh and the candidate who is appearing at the examination or the due paper(s) is considered as backlog.

14. Meaning of abbreviation used in the mark sheet is as given below :

P-Pass	F-Fail in aggregate	A Absent
DE - Due in external	PG-Pass with grade	DA-Due in assignment

15. All disputes relating to admission, study, Examination, issue of result, certificates and etc., are govered by Civil Laws and Civil Courts only subject to Chennai Jurisdiction.

INDIAN INSTITUTE OF INTEGRATED SCIENCE AND TECHNOLOGY

An Independent Institute Established by Sri Shirdi Sai Educational Trust (Regd)

Chennai , Tamilnadu, India

RESULT CUM - CONSOLIDATED MARKS CARD

Name of the Candidate	TEKI . GANESH	Father's Name	TEKI.MALLIKARJUNARAO
Programme	BSC ELECTRONICS	D.O.B	07 - 05 - 1970
Semester / Year	Consolidated	Session	2013 - 2016
Status	Fresh	Roll No.	610B16ELE001

Subject Code	Subject	Marks obtained	Minimum Pass Marks	Maximum Marks
HSE	TECHNICAL ENGLISH	76	50	100
MAT	MATHEMATICS	74	50	100
PHY	PHYSICS	77	50	100
CYE	CHEMISTRY	61	50	100
GEC	COMPUTERPROGRAMMING	69	50	100
GEE	DIGITAL SYSTEM DESIGN	60	50	100
GEB	BASIC ELECTRICAL AND ELECTRONICS	79	50	100
SIS	SIGNALS & SYSTEMS	75	50	100
ANE	ANALOG ELECTRONICS	77	50	100
GEP	GENERAL PROFICIENCY	69	50	100
DIE	DIGITAL ELECTRONICS	59	50	100
COS	COMMUNICATION SYSTEMS	70	50	100
EMT	ELECTRO MAGNETIC THEORY	73	50	100
THD	THERMODYNAMICS	76	50	100
COE	COMPUTER NETWORKS	74	50	100
ELM	ELECTRONIC MEASUREMENT	66	50	100
CSE	CONTROL SYTEMS	63	50	100
AWP	ANTENNA AND WAVE PROPAGATION	65	50	100
CRO	COMPUTER ARCHITECTURE AND ORG	68	50	100
MAI	MICROPROCESSORS AND INTERFACING	57	50	100
MIC	MICROPROCESSORS	72	50	100
TOTAL		1460	1050	2100
DIVISION				First

Date : 29.07.2016

Registrar / Controller of Examination