



INDIAN INSTITUTE OF INTEGRATED SCIENCE AND TECHNOLOGY

An Independent Institute Established by Sri Shirdi Sai Educational Trust (Regd)
Chennai, Tamilnadu, India

RESULT CUM - DETAILED MARKS CARD

Name of the Candidate	D.PRAVEEN	Father's Name	DEVENDIRAN.T
Programme	BE EEE (LATERAL ENTRY)	D.O.B	30.08.1990
Semester / Year	Consolidated Mark Sheet	Session	2011-2014
Status	Regular	Roll No.	91D24BEEEE2014

Subject Code	Subject	Marks obtained	Minimum Pass Marks	Maximum Marks
ECT	ELECTRICAL CIRCUIT THEORY	63	50	100
ELM	ELECTRICAL MACHINES-I	67	50	100
EDC	ELECTRONIC DEVICES AND CIRCUITS	76	50	100
WOS	WORK SHOP PRACTICAL	72	50	100
COA	COMPUTER APPLICATIONS	70	50	100
ELM	ELECTRICAL MACHINES-II	77	50	100
MIC	POWER SYSTEM	73	50	100
SEM	MICRO CONTROLLER	69	50	100
ADC	SPECIAL ELECTRICAL MACHINES	70	50	100
ECS	ADVANCED COMMUNICATION	76	50	100
CEM	ELECTRICAL CIRCUITS SIMULATION	72	50	100
PLC	CONTROL OF ELECTRICAL MACHINES	70	50	100
MIP	PROGRAMMABLE LOGIC CONTROLLER	73	50	100
FFA	ARTIFICIAL NEURAL NETWORKS	69	50	100
FUS	ADVANCED CONTROL THEORY	67	50	100
BUC	POWER GENERATION SYSTEMS	66	50	100
PBE	MODERN POWER SYSTEM ANALYSIS	70	50	100
BMS	HIGH VOLTAGE ENGINEERING	76	50	100
BUE	SPECIAL ELECTRICAL MACHINES	72	50	100
COL	PROJECT	85	50	100
TOTAL DIVISION		1433	1000	2000 First

Entered by

Date : 28.07.2014

Registrar / Controller of Examination