



INDIAN INSTITUTE OF INTEGRATED SCIENCE AND TECHNOLOGY

An Independent Institute Established by Sri Shirdi Sai Educational Trust (Regd)
Chennai, Tamilnadu, India

RESULT CUM - DETAILED MARKS CARD

Name of the Candidate	SHIMPI SANDIP KAMLAKAR	Father's Name	KAMLAKAR J.SHIMPI	
Programme	B.TECH EEE (LATERAL ENTRY)	D.O.B	19.05.1976	
Semester / Year	Consolidated Mark Sheet	Session	2015-2018	
Status	Distance Education	Roll No.	7130BEEE2018	
Subject Code	Subject	Marks obtained	Minimum Pass Marks	Maximum Marks
ECT	ELECTRICAL CIRCUIT THEORY	60	50	100
ELM	ELECTRICAL MACHINES-I	70	50	100
EDC	ELECTRONIC DEVICES AND CIRCUITS	72	50	100
WOS	WORK SHOP PRACTICAL	70	50	100
COA	COMPUTER APPLICATIONS	66	50	100
ELM	ELECTRICAL MACHINES-II	69	50	100
PST	POWER SYSTEM	65	50	100
MCN	MICRO CONTROLLER	70	50	100
SEM	SPECIAL ELECTRICAL MACHINES	72	50	100
AC	ADVANCED COMMUNICATION	69	50	100
ECS	ELECTRICAL CIRCUITS SIMULATION	71	50	100
CEM	CONTROL OF ELECTRICAL MACHINES	60	50	100
PLC	PROGRAMMABLE LOGIC CONTROLLER	74	50	100
ANN	ARTIFICIAL NEURAL NETWORKS	71	50	100
ACT	ADVANCED CONTROL THEORY	73	50	100
PGS	POWER GENERATION SYSTEMS	69	50	100
MPS	MODERN POWER SYSTEM ANALYSIS	70	50	100
HVE	HIGH VOLTAGE ENGINEERING	63	50	100
SEM	SPECIAL ELECTRICAL MACHINES	70	50	100
PRO	PROJECT	76	50	100
TOTAL DIVISION		1380	1000	2000 First

Entered by

Date : 28.07.2018


Registrar / Controller of Examination