



INDIAN INSTITUTE OF INTEGRATED SCIENCE AND TECHNOLOGY

An Independent Institute Established by Sri Shirdi Sai Educational Trust (Regd)
Chennai, Tamilnadu, India

RESULT CUM - DETAILED MARKS CARD

Name of the Candidate	C.JAYAPRAKASH	Father's Name	CHANDRAN.M
Programme	BE EEE (LATERAL ENTRY)	D.O.B	02.02.1992
Semester / Year	Consolidated Mark Sheet	Session	2011-2014
Status	Regular	Roll No.	91D25BEEE2014

Subject Code	Subject	Marks obtained	Minimum Pass Marks	Maximum Marks
ECT	ELECTRICAL CIRCUIT THEORY	76	50	100
ELM	ELECTRICAL MACHINES-I	70	50	100
EDC	ELECTRONIC DEVICES AND CIRCUITS	79	50	100
WOS	WORK SHOP PRACTICAL	72	50	100
COA	COMPUTER APPLICATIONS	80	50	100
ELM	ELECTRICAL MACHINES-II	72	50	100
PST	POWER SYSTEM	76	50	100
MCN	MICRO CONTROLLER	70	50	100
SEM	SPECIAL ELECTRICAL MACHINES	76	50	100
AC	ADVANCED COMMUNICATION	72	50	100
ECS	ELECTRICAL CIRCUITS SIMULATION	79	50	100
CEM	CONTROL OF ELECTRICAL MACHINES	77	50	100
PLC	PROGRAMMABLE LOGIC CONTROLLER	74	50	100
ANN	ARTIFICIAL NEURAL NETWORKS	70	50	100
ACT	ADVANCED CONTROL THEORY	76	50	100
PGS	POWER GENERATION SYSTEMS	75	50	100
MPS	MODERN POWER SYSTEM ANALYSIS	76	50	100
HVE	HIGH VOLTAGE ENGINEERING	75	50	100
SEM	SPECIAL ELECTRICAL MACHINES	70	50	100
PRO	PROJECT	88	50	100
TOTAL DIVISION		1503	1000	2000 First

Entered by

Date: 28.07.2014


Registrar / Controller of Examination