



INDIAN INSTITUTE OF INTEGRATED SCIENCE AND TECHNOLOGY

An Independent Institute Established by Sri Shirdi Sai Educational Trust (Regd)
Chennai, Tamilnadu, India

RESULT CUM - DETAILED MARKS CARD

Name of the Candidate	K.JAYANTH	Father's Name	K.S.HARINATH
Programme	BE ECE	D.O.B	02.06.1995
Semester / Year	Consolidated - I	Session	2012-2016
Status	Regular	Roll No.	152111262012

Subject Code	Subject	Marks obtained	Minimum Pass Marks	Maximum Marks
SEM-I				
ENG	English	70	50	100
CAL	Calculus	73	50	100
PHY	Physics	79	50	100
MOS	Mechanics of Solids	72	50	100
EGR	Engineering Graphics	78	50	100
MWS	Mechanical Workshop - I	70	50	100
PHP	Physics Practical	72	50	100
SEM-II				
LAL	Linear Algebra	73	50	100
CHE	Chemistry	71	50	100
EES	Environment and Energy Studies	75	50	100
AOP	Art of Programming	70	50	100
EEE	Elements of Electrical Engineering	69	50	100
ELW	Electrical Workshop	78	50	100
CHP	Chemistry Practical	71	50	100
SEM-III				
EDC	Electronics Devices and Circuits-I	73	50	100
DCR	Digital Circuits	79	50	100
NWA	Network Analysis	79	50	100
LCS	Linear Control System	75	50	100
VCL	Vector Calculus	72	50	100
EFE	Economics For Engineers	70	50	100
ADC	Analog and Digital Circuits Lab	71	50	100
FDS	Fundamentals of Data Structures in C Laboratory	75	50	100
SEM-IV				
PRD	Probability Distributions	73	50	100
SAS	Signals and Systems	71	50	100
EDC	Electronics Devices and Circuits - II	75	50	100
EMD	Electrical Machines and Drives	75	50	100
EDT	Elect. Design, Tools and Packages	76	50	100
ITS	ICT Tools and Security	78	50	100
CDS	Circuits Design and Simulation Lab	75	50	100
LIC	Linear Integrated Circuits Lab	70	50	100
TOTAL				
DIVISION				

CONTINUED IN CONSOLIDATED - II

Entered by

Date : 28.07.2016

Registrar / Controller of Examination



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Subject Code	Subject	Marks obtained	Minimum Pass Marks	Maximum Marks
SEM-V				
EME	Electromagnetics Engineering	72	50	100
ICA	Integrated Circuits and Applications	76	50	100
MCP	Microprocessor	75	50	100
MMI	Modern Measurement and Instruments	79	50	100
DCM	Digital Communication	76	50	100
DSP	Digital Signal Processing	77	50	100
DSL	Digital Signal Processing Lab	75	50	100
CSL	Communication Systems Lab	79	50	100
NWL	Networks Laboratory	74	50	100
SEM-VI				
DSD	Digital System Design	69	50	100
AWP	Antenna and Wave Propagation	67	50	100
FOC	Fiber Optic Communication	64	50	100
SMD	System Modeling and Design	72	50	100
TCN	Telecom Networks	68	50	100
VLS	VLSI Design Laboratory	79	50	100
MML	Microprocessors and Microcontrollers Laboratory	77	50	100
SEM-VII				
EMS	Embedded Systems	72	50	100
MWE	Microwave Engineering	76	50	100
MMS	Multimedia Systems	75	50	100
WLC	Wireless Communications	75	50	100
WSN	Wireless Sensor Networks	73	50	100
IMP	Image Processing	79	50	100
EML	Embedded Laboratory	72	50	100
ACL	Advanced Communication Lab	70	50	100
SEM-VIII				
RFC	RF Communication Circuits	72	50	100
BBN	Broadband Network	76	50	100
CPH	Comprehensive Viva/Voce	75	50	100
SCM	Satellite Communication	74	50	100
PRO	Major Project	90	50	100
TOTAL		4366	2950	5900
DIVISION				First

Entered by

Date : 28.07.2016


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