



INDIAN INSTITUTE OF INTEGRATED SCIENCE AND TECHNOLOGY

An Independent Institute Established by Sri Shirdi Sai Educational Trust (Regd)
Chennai, Tamilnadu, India

RESULT CUM - DETAILED MARKS CARD

Name of the Candidate	VIJAY .P	Father's Name	PURUSHOTHAMAN
Programme	Diploma In EEE	D.O.B	13.09.1994
Semester / Year	Consolidated	Session	2011-2014
Status	Regular	Roll No.	61T32DEEE2011

Subject Code	Subject	Marks obtained	Minimum Pass Marks	Maximum Marks
ENG	English	78	50	100
ENM	Engineering Mathematics	76	50	100
ENP	Engineering Chemistry	70	50	100
ENC	Basic Electrical Engineering	71	50	100
BEE	Mechanical workshop	69	50	100
MEW	Engineering Physics	74	50	100
END	Engineering Drawing	64	50	100
INT	Information Technology	75	50	100
ECT	Electrical Circuit Theory	64	50	100
ELM	Electrical Machines-I	70	50	100
EDC	Electronic Devices and Circuits	74	50	100
WSP	Work Shop Practical	78	50	100
CMP	Computer Applications	73	50	100
EMH	Electrical Machines-II	75	50	100
MAI	Measurements and Instruments	70	50	100
PST	Power System	75	50	100
MCN	Micro Controller	70	50	100
SEM	Special Electrical Machines	74	50	100
ADC	Advanced Communication	66	50	100
ECS	Electrical Circuits Simulation	71	50	100
CEM	Control of Electrical Machines	74	50	100
PLC	Programmable Logic Controller	70	50	100
PRO	Project	85	50	100
TOTAL		1666	1150	2300
DIVISION				First

Entered by

Date : 25.07.2014


Registrar / Controller of Examination