



INDIAN INSTITUTE OF INTEGRATED SCIENCE AND TECHNOLOGY

An Independent Institute Established by Sri Shirdi Sai Educational Trust (Regd)
Chennai, Tamilnadu, India

RESULT CUM - DETAILED MARKS CARD

Name of the Candidate	VADIVEL. K	Father's Name	KANNAN
Programme	DIPLOMA IN ECE	D.O.B	11.06.1986
Semester / Year	CONSOLIDATED	Session	2005-2007
Status	PART TIME (LATERAL ENTRY)	Roll No.	61C42DECE2005

Subject Code	Subject	Marks obtained	Minimum Pass Marks	Maximum Marks
YEAR-II				
COE	Communication Engineering	76	50	100
ELM	Electronic Measurement	73	50	100
DSP	Antenna And Wave Propagation	73	50	100
AWP	Computer Architecture And Org	75	50	100
CRO	Microprocessors And Interfacing	76	50	100
MAI	Digital Signal Processing	78	50	100
MIC	Microprocessors	81	50	100
YEAR-III				
MAR	Microwave And Radar	76	50	100
CSE	Control Sytems Engg	83	50	100
VLD	Computer Networks	73	50	100
CON	Digital System Design	75	50	100
DSD	Microcontroller	75	50	100
MIC	VLSI Design	76	50	100
EMS	Embedded System	81	50	100
PRT	Project	74	50	100
TOTAL		1145	750	1500
DIVISION				First

Entered by

Date: 20.07.2007

Registrar / Controller of Examination