



INDIAN INSTITUTE OF INTEGRATED SCIENCE AND TECHNOLOGY

An Independent Institute Established by Sri Shirdi Sai Educational Trust (Regd)
Chennai , Tamilnadu, India

RESULT CUM - DETAILED MARKS CARD

Name of the Candidate	SRIDHAR .C	Father's Name	CHANDRASEKER .J
Programme	DIPLOMA IN EEE	D.O.B	03.01.1990
Semester / Year	CONSOLIDATED	Session	2014-2016
Status	PART TIME (LATERAL ENTRY)	Roll No.	1624DME2014

Subject Code	Subject	Marks obtained	Minimum Pass Marks	Maximum Marks
YEAR-II				
ECT	Electrical Circuit Theory	64	50	100
ELM	Electrical Machines-I	70	50	100
EDC	Electronic Devices and Circuits	74	50	100
WSP	Work Shop Practical	78	50	100
CMP	Computer Applications	73	50	100
EMH	Electrical Machines-II	75	50	100
MAI	Measurements and Instruments	70	50	100
YEAR-III				
PST	Power System	75	50	100
MCN	Micro Controller	70	50	100
SEM	Special Electrical Machines	74	50	100
ADC	Advanced Communication	66	50	100
ECS	Electrical Circuits Simulation	71	50	100
CEM	Control of Electrical Machines	74	50	100
PLC	Programmable Logic Controller	70	50	100
PRO	Project	85	50	100
TOTAL		1089	750	1500
DIVISION				First

Entered by

Date : 25.07.2016


Registrar / Controller of Examination