



INDIAN INSTITUTE OF INTEGRATED SCIENCE AND TECHNOLOGY

An Independent Institute Established by Sri Shirdi Sai Educational Trust (Regd)
Chennai, Tamilnadu, India

RESULT CUM - DETAILED MARKS CARD

Name of the Candidate	YARRABOTHULA PRASANTH	Father's Name	Y.VENKATESULU
Programme	B.TECH COMPUTER SCIENCE ENGG	D.O.B	04.02.1995
Semester / Year	CONSOLIDATED- I	Session	2012-2016
Status	PASS	Roll No.	15355BCS2012

Subject Code	Subject	Marks obtained	Minimum Pass Marks	Maximum Marks
SEM-I				
EN-I	English - I	71	50	100
EM-I	Engineering Mathematics – I	62	50	100
EC-I	Engineering Chemistry - I	74	50	100
EP-I	Engineering Physics - I	61	50	100
CIT	Computers and Information Technology	72	50	100
CDS	C & Data Structures	71	50	100
PHL	Physics Laboratory	76	50	100
CHL	Chemistry Laboratory	88	50	100
EPL	Engineering Practices Laboratory	82	50	100
SEM-II				
EN-II	English - II	69	50	100
EM-II	Engineering Mathematics – II	73	50	100
EC-II	Engineering Chemistry - II	72	50	100
EP-II	Engineering Physics - II	71	50	100
NWT	Network Theory	61	50	100
SDC	Semiconductor Devices and circuits	75	50	100
DGL	Digital Laboratory	80	50	100
PGL	Programming Laboratory	72	50	100
SEM-III				
EM-III	Engineering Mathematics – III	71	50	100
DSG	Discrete Structures and Graph Theory	66	50	100
DPF	Data Processing and File Organization	66	50	100
LDI	Linear and Digital ICs Applications	71	50	100
TOTAL				
DIVISION				

Entered by

CONTINUED IN CONSOLIDATED- II

Date : 25.07.2016

Registrar / Controller of Examination



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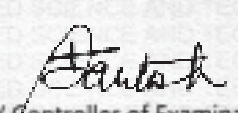
Name of the Candidate	YARRABOTHULA PRASANTH	Father's Name	Y.VENKATESULU
Programme	B.TECH COMPUTER SCIENCE ENGG	D.O.B	04.02.1995
Semester / Year	CONSOLIDATED- II	Session	2012-2016
Status	PASS	Roll No.	15355BCS2012

Subject Code	Subject	Marks obtained	Minimum Pass Marks	Maximum Marks
STL	Switching theory and logic design	73	50	100
MEA	Managerial Economics and Accountancy	69	50	100
DSL	Data Structures Lab	81	50	100
CPL	Computer Practices Laboratory	82	50	100
SEM-IV				
PAS	Probability and Statistics	62	50	100
ETC	Electrical Technology	71	50	100
CPO	Computer Organization	62	50	100
DAA	Design and Analysis of Algorithms	70	50	100
OSY	Operating Systems	61	50	100
OOP	Object Oriented Programming	66	50	100
JIL	Java and Internet Programming Lab	75	50	100
OSL	Operating Systems Lab	80	50	100
SEM-V				
ITM	Interfacing through Microprocessors	63	50	100
OPR	Operations Research	71	50	100
DCM	Data Communications	68	50	100
TOC	Theory of Computation	61	50	100
PPL	Principles of Programming Languages	70	50	100
SPR	System Programming	66	50	100
CNL	Communications and Networks Lab	81	50	100
MPL	Microprocessors Lab	79	50	100
TOTAL				
DIVISION				

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CONTINUED IN CONSOLIDATED- III

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Name of the Candidate	YARRABOTHULA PRASANTH	Father's Name	Y.VENKATESULU
Programme	B.TECH COMPUTER SCIENCE ENGG	D.O.B	04.02.1995
Semester / Year	CONSOLIDATED- III	Session	2012-2016
Status	PASS	Roll No.	15355BCS2012

Subject Code	Subject	Marks obtained	Minimum Pass Marks	Maximum Marks
SEM-VI				
MNS	Management Science	71	50	100
CGP	Computer graphics	76	50	100
CNW	Computer Networks	70	50	100
AIT	Artificial Intelligence	66	50	100
LPR	Language processors	62	50	100
DMS	Database management Systems	70	50	100
CGL	Computer Graphics and Multimedia Lab	70	50	100
DML	Database Management Systems Lab	81	50	100
CIP	Creative and Innovative Project	76	50	100
SEM-VII				
SEN	Software Engineering	64	50	100
DSY	Distributed Systems	69	50	100
VPT	Visual Programming Techniques	61	50	100
JPR	Java programming	70	50	100
ACA	Advanced Computer Architecture	74	50	100
DSI	Digital Speech and Image processing	60	50	100
MAL	Mobile Application Development Lab	80	50	100
SDL	Software Development Lab	83	50	100
SEM-VIII				
SAM	Simulation and Modelling	71	50	100
ROB	Robotics	70	50	100
DMW	Data Mining and Ware Housing	75	50	100
ADB	Advanced Databases.	61	50	100
PRO	Project Work	75	50	100
TOTAL		4470	3150	6300
DIVISION				First

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