

# INDIAN INSTITUTE OF INTEGRATED SCIENCE AND TECHNOLOGY

An Independent Institute Established by Trust Regd. Under Govt. India

### **RESULT CUM - DETAILED MARKS CARD**

Name of the Candidate	SIRVISETTY CHANDRA SEKAR	Father's Name	SIRVISETTY VENKATESWARA RAO
Programme	B.TECH ECE	D.O.B	28.02.1998
Semester / Year	CONSOLIDATED-I	Session	2015-2019
Centre Code	TNK200080	Roll No.	1538BTECE2015

Subject	Subject	Marks	Minimum Pass	Maximum
Code	Subject	obtained	Marks	Marks
SEM-I		dian instituti	o integral at a	cience and Teo
ENG	English	64	50	100
CAL	Calculus	76	50	100
PHY	Physics	72	50	100
MOS	Mechanics of Solids	69	50	100
EGR	Engineering Graphics	71	50	100
CPL	Computer Practices Lab	76	50	100
EPL	Engineering Practices Lab	80	50	100
PCL	Physics and Chemistry Lab - I	79	50	100
SEM-II	Linear Algebra	69	50	100
CHE	Chemistry	70	50	100
EES	Environment and Energy Studies	71	50	100
AOP	Art of Programming	73	50	100
EEE	Elements of Electrical Engineering	70	50	100
PCL	to to a service to a summatter of some particular to	81	50	100
CDL	Physics and Chemistry Lab- II	79	50	100
SEM-III	Circuits and Devices Lab	79	50	100
EDC	Electronics Devices and Circuits-I	69	50	100
DCR	Digital Circuits	70	50	100
NWA	Network Analysis	71	50	100
LCS	Linear Control System	73	50	100
VRC	Vector Calculus	80	50	100
EFE	Economics For Engineers	77	50	100
ADC	Analog and Digital Circuits Lab	80	50	100
ODS	OOPS and Data Structures Lab	77	50	100
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DIVISION	integrated Screens and Technology II	dien les arch	nt Internated 9	rience and Tes

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Date:

25.07.2019

Registrar / Controller of Examination



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Centre Code	TNK200080	Roll No.	1538BTECE2015	

Subject	Subject	Marks	Minimum Pass	Maximum
Code	Subject	obtained	Marks	Marks
SEM-IV	Integrals a series and release on the	cian in the	50	100
PDT	Probability Distributions	69	50	
SAS	Signals and Systems	78	50	100
EDC	Electronics Devices and Circuits - II	74	50	100
EMD	Electrical Machines and Drives	77	50	Mence 100 Le
ETP	Electrical Design, Tools and Packages	72	50	100
ICT	ICT Tools and Security	76	50	100
CSI	Circuit and Simulation Integrated Lab	81	50	100
LEC	Linear Integrated Circuit Lab	80	50	100
EEC	Electrical Engineering and	77	50	100
tistitute or	Control System Lab	kingsharus usga silan lagularin	O ASIASSI GISAL Makilokasayan	tulet mes analyticks onemes analyticks
SEM-V EME	Electromagnetics Engineering	66	50	100
ICA	Integrated Circuits and Applications	69	50	100
BEHILD ST		74	50	100
MIP	Microprocessor	State Land In	The second of the	Concerns one of Texas
MMI	Modern Measurement and Instruments	70	50	100
DCM	Digital Communication	75	50	100
DSP	Digital Signal Processing	79	50	100
DSL	Digital Signal Processing Lab	80	50	100
CSL	Communication System Lab	83	50	100
MML	Microprocessor and Microcontroller Lab	79	50	100
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Semester / Year	CONSOLIDATED- III	Session	2015-2019
Centre Code	TNK200080	Roll No.	1538BTECE2015

Subject Code	Subject	Marks obtained	Minimum Pass Marks	Maximum Marks
SEM-VI	tegration science and reletatory) is	70	50	100
DSD	Digital System Design	76	50	100
AWP	Antenna and Wave Propagation	79	and the second and the	100
FOC	Fiber Optic Communication	than trooping	50	100
SMD	System Modeling and Design	77	50	100
TLN	Telecom Networks	71	50	
VSL	VLSI Design Lab	80	50	100
CNL	Computer Networks Lab	81	50	100
CSL	Communication and Soft Skills Lab	85	50	100
SEM-VII EMS	Embedded Systems	77	50	100
MWE	Microwave Engineering	69	50	100
MMS	Multimedia Systems	70	50	100
WLC	Wireless Communications	79	50	100
WSN	Wireless Sensor Networks	72	50	100
IMP	Image Processing	70	50	100
EML	Embedded Lab	79	50	100
OML	Optical and Microwave Lab	82	50	100
SEM-VIII RFC	RF Communication Circuits	79	50	100
BBN	Broadband Network	77	50	100
SCM	Satellite Communication	73	50	100
PRO	Project Project	85	50	100
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