

INDIAN INSTITUTE OF INTEGRATED SCIENCE AND TECHNOLOGY

An Independent Institute Established by Sri Shirdi Sai Educational Trust (Regd)
Chennai , Tamilnadu, India

RESULT CUM - DETAILED MARKS CARD

Name of the Candidate	VIGNESHWARA PANDIAN A	Father's Name	ANANDAPANDIAN
Programme	B.TECH ECE	D.O.B	28.05.1995
Semester / Year	CONSOLIDATED - I	Session	2013-2016
Status	REGULAR (LATERAL ENTRY)	Roll No.	1579BECE2013

Subject Subject		Marks	Minimum Pass	Maximum
Code	,	obtained	Marks	Marks
SEM-III	ne elektronen en	AN INSTITUTE (
EDC	Electronics Devices and Circuits-I	69	50	100
DCR	Digital Circuits	70	50	100
NWA	Network Analysis	71	50	100
LCS	Linear Control System	73	50	100
permit at	INTERNATIONAL PROPERTY OF AND THE DRUCK GRANING	80	50	100
VRC	Vector Calculus			
EFE	Economics For Engineers	77	50	100
SEM-IV	HITE GRATED SCHINGE AND TECHNOLOGY IND	ANCING BY UTEX	Fill Borness	NOE AND TECHNO
PDT	Probability Distributions	69	50	100
SAS	Signals and Systems	78	50	100
EDC	Electronics Devices and Circuits - II	74	50	100
EMD	Electrical Machines and Drives	77	50	100 M
ETP	Electrical Design, Tools and Packages	72	50	100
ICT	ICT Tools and Security	76	50	100
SEM-V	INTERPATED SOEMOS AND TECHNOLOGY RED	AN IN SHITUTE	F MILEOSANI LICE	NOT AND TECHNO
EME	Electromagnetics Engineering	66	50	100
ICA	Integrated Circuits and Applications	69	50	100
MIP	Microprocessor	74	50	100
MMI	Modern Measurement and Instruments	70	50	100
DCM	Digital Communication	75	50	100
DSP	Digital Signal Processing	79	50	100
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Entered by

Date:

25.07.2016

Registrar / Controller of Examination



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Subject	Subject	Marks	Minimum Pass	Maximum
Code	Susject	obtained	Marks	Marks
SEM-VI	TESPATEL SHENCE AND ECHICAVORY	LAN INSTITUTE		NCT AND THE HILL
DSD	Digital System Design	70	50	100
AWP	Antenna and Wave Propagation	76	50	100
FOC	Fiber Optic Communication	79	50	100
SMD	System Modeling and Design	77	50	100
TLN	Telecom Networks	71	50	100
VSL	VLSI Design Laboratory	80	50	100
SEM-VII	TELLIA TELD SCIENCE AND TELLINGED AND	E AN INSTITUTE I		
EMS	Embedded Systems	77	50	100
MWE	Microwave Engineering	69	50	100
MMS	Multimedia Systems	70	50	100
WLC	Wireless Communications	79	50	100
WSN	Wireless Sensor Networks	72	50	100
IMP	Image Processing	70	50	100
SEM-VIII	TEGRATED SCHOOL AND TECHNOLOGY A		50	100
RFC	RF Communication Circuits	79		
BBN	Broadband Network	77	50	100
SCM	Satellite Communication	73	50	100
PRO	Major Project	85	50	100
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TOTAL DIVISION	TEGRATED SCIENCE AND TECHNOLOGY IS TEGRATED SCIENCE AND TECHNOLOGY IS TEGRATED SCIENCE AND TECHNOLOGY IS TEGRATED SCIENCE AND TECHNOLOGY IS	2523	1700	3400 First

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Date: 25.07.2016

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