



INDIAN INSTITUTE OF INTEGRATED SCIENCE AND TECHNOLOGY

An Independent Institute Established by Sri Shirdi Sai Educational Trust (Regd)
Chennai, Tamilnadu, India

RESULT CUM - DETAILED MARKS CARD

Name of the Candidate	VIGNESHWARA PANDIAN A	Father's Name	ANANDAPANDIAN
Programme	B.TECH ECE	D.O.B	28.05.1995
Semester / Year	CONSOLIDATED - I	Session	2013-2016
Status	REGULAR (LATERAL ENTRY)	Roll No.	1579BECE2013

Subject Code	Subject	Marks obtained	Minimum Pass Marks	Maximum Marks
SEM-III				
EDC	Electronics Devices and Circuits-I	69	50	100
DCR	Digital Circuits	70	50	100
NWA	Network Analysis	71	50	100
LCS	Linear Control System	73	50	100
VRC	Vector Calculus	80	50	100
EFE	Economics For Engineers	77	50	100
SEM-IV				
PDT	Probability Distributions	69	50	100
SAS	Signals and Systems	78	50	100
EDC	Electronics Devices and Circuits - II	74	50	100
EMD	Electrical Machines and Drives	77	50	100
ETP	Electrical Design, Tools and Packages	72	50	100
ICT	ICT Tools and Security	76	50	100
SEM-V				
EME	Electromagnetics Engineering	66	50	100
ICA	Integrated Circuits and Applications	69	50	100
MIP	Microprocessor	74	50	100
MMI	Modern Measurement and Instruments	70	50	100
DCM	Digital Communication	75	50	100
DSP	Digital Signal Processing	79	50	100
TOTAL				
DIVISION				

CONTINUED IN CONSOLIDATED- II

Entered by

Date : 25.07.2016

Registrar / Controller of Examination



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Subject Code	Subject	Marks obtained	Minimum Pass Marks	Maximum Marks
SEM-VI				
DSD	Digital System Design	70	50	100
AWP	Antenna and Wave Propagation	76	50	100
FOC	Fiber Optic Communication	79	50	100
SMD	System Modeling and Design	77	50	100
TLN	Telecom Networks	71	50	100
VSL	VLSI Design Laboratory	80	50	100
SEM-VII				
EMS	Embedded Systems	77	50	100
MWE	Microwave Engineering	69	50	100
MMS	Multimedia Systems	70	50	100
WLC	Wireless Communications	79	50	100
WSN	Wireless Sensor Networks	72	50	100
IMP	Image Processing	70	50	100
SEM-VIII				
RFC	RF Communication Circuits	79	50	100
BBN	Broadband Network	77	50	100
SCM	Satellite Communication	73	50	100
PRO	Major Project	85	50	100
TOTAL		2523	1700	3400
DIVISION				First

Entered by

Date: 25.07.2016


Registrar / Controller of Examination