

## INDIAN INSTITUTE OF INTEGRATED SCIENCE AND TECHNOLOGY

An Independent Institute Established by Trust Regd. Under Govt. India

## **RESULT CUM - DETAILED MARKS CARD**

Name of the Candidate	RAMASWAMY PEDDI	Father's Name	VENKATESWARA RAO		
Programme	B.TECH ECE (LATERAL ENTRY)	D.O.B	10.10.1984		
Semester / Year	CONSOLIDATED	Session	2003-2006		
Centre Code	TNK200080	Roll No.	1590BTECE2003		

Subject Code	Subject	Marks obtained	Minimum Pass Marks	Maximum Marks
Code		optamed .	IVIaIKS	IVIAIRS
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YEAR-II	egratera balence and Jechik Jody III	Great to autient	50	100
SIS	SIGNALS & SYSTEMS	79	· 50 50	100
ANE	ANALOG ELECTRONICS	75	50	100
DIE	DIGITAL ELECTRONICS COMMUNICATION SYSTEMS	69 65	50	100
EMT	ELECTRO MAGNETIC THEORY	74	50	100
THD	THERMODYNAMICS	74	50	100
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estitute of in		olan incinut	O D OFFICE S	
YEAR-III COE	COMMUNICATION ENGINEERING	78	50	100
ELM	ELECTRONIC MEASUREMENT	74	50	100
AWP	ANTENNA AND WAVE PROPAGATION	83	50	100
CRO	COMPUTER ARCHITECTURE AND	73	50	100
OKO I	ORGANIZATION	13 01	30	100
MAI	MICROPROCESSORS & INTERFACING	75	50	100
MIC	MICROPROCESSORS	75	50	100
YEAR-IV		dian institut	of imagnited t	
MAR	MICROWAVE AND RADAR	73	50	100
CSE	CONTROL SYTEMS ENGINEERING	75	50	100
CNP	COMPUTER NETWORKS	69	50	100
DSD	DIGITAL SYSTEM DESIGN	71	50	100
MIC	MICROCONTROLLER	65	50	100
VID	VLSI DESIGN	59	50	100
EMS	EMBEDDED SYSTEM	70	50	100
PRT	PROJECT	75	of Integ 50 ed	100
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TOTAL	lagrama ocience end recimology in	1451	1000	2000
DIVISION				First

Entered by

Date: 27.07.2006

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Registrar / Controller of Examination