

INDIAN INSTITUTE OF INTEGRATED SCIENCE AND TECHNOLOGY

An Independent Institute Established by Trust Regd. Under Govt. India

RESULT CUM - DETAILED MARKS CARD

The state of the s				
Name of the Candidate	VENTRAPRAGADA HARI NAGA VENKATA SAI PAVAN	Father's Name	KISHORE KUMAR	
Programme	DIPLOMA IN EEE	D.O.B	19.11.1997	
Semester / Year	CONSOLIDATED	Session	2014-2017	
Centre Code	TNA200030	Roll No.	15379DEEE2004	

Subject	Subject	Marks obtained	Minimum Pass	Maximum
Code		obtained	Marks	Marks
YEAR-I		a sa fraillean	of Integrated S	
ENG	English	85	50	100
ENM	Engineering Mathematics	75	50	100
ENC	Engineering Chemistry	85	50	100
BEE	Basic Electrical Engineering	89	50	100
ENP	Engineering Physics	84	50	100
END	Engineering Drawing	87	50	100
YEAR-II			30 5	
ECT	Electrical Circuit Theory	84	50	100
ELM	Electrical Machines	86	50	100 '
EDC	Electronic Devices and Circuits	84	50	100
CMP	Computer Applications	83	50	100
ECS	Electrical Circuits Simulation	85	50	100
MAI	Measurements and Instruments	87	50	100
YEAR-III	ated Science and		of integrated S	
PST	Power System	85	50	100
MCN	Micro Controller	89	50	100
SEM	Special Electrical Machines	84	50	100
CEM	Control of Electrical Machines	88	50	100
PLC	Programmable Logic Controller	83	50	100
PRO	Project	95	50	100
TOTAL DIVISION		1538	900	1800 First

Entered by

Date: __25.06.2017

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Registrar / Controller of Examination