



# INDIAN INSTITUTE OF INTEGRATED SCIENCE AND TECHNOLOGY

An Independent Institute Established by Trust Regd. Under Govt. India

## RESULT CUM - DETAILED MARKS CARD

Name of the Candidate	DURAIRAJ G	Father's Name	GAJENDRAN G
Programme	B.TECH EEE (LATERAL ENTRY)	D.O.B	24.10.1987
Semester / Year	CONSOLIDATED	Session	2008-2011
Centre Code	TAN200110	Roll No.	7199BTEEE2008

Subject Code	Subject	Marks obtained	Minimum Pass Marks	Maximum Marks
<b>YEAR-II</b>				
ECT	ELECTRICAL CIRCUIT THEORY	70	50	100
ELM	ELECTRICAL MACHINES-I	72	50	100
EDC	ELECTRONIC DEVICES & CIRCUITS	64	50	100
WOS	WORK SHOP PRACTICAL	69	50	100
COA	COMPUTER APPLICATIONS	72	50	100
ELM	ELECTRICAL MACHINES-II	70	50	100
<b>YEAR-III</b>				
PWS	POWER SYSTEM	70	50	100
MIC	MICRO CONTROLLER	64	50	100
SEM	SPECIAL ELECTRICAL MACHINES	69	50	100
ADC	ADVANCED COMMUNICATION	67	50	100
ECM	ELECTRICAL CIRCUITS SIMULATION	72	50	100
CEM	CONTROL OF ELECTRICAL MACHINES	73	50	100
PLC	PROGRAMMABLE LOGIC CONTROLLER	71	50	100
<b>YEAR-IV</b>				
ANN	ARTIFICIAL NEURAL NETWORKS	70	50	100
ADT	ADVANCED CONTROL THEORY	72	50	100
PGS	POWER GENERATION SYSTEMS	73	50	100
MPS	MODERN POWER SYSTEM ANALYSIS	71	50	100
HVE	HIGH VOLTAGE ENGINEERING	75	50	100
SEM	SPECIAL ELECTRICAL MACHINES	69	50	100
PRO	PROJECT	85	50	100
<b>TOTAL DIVISION</b>		<b>1418</b>	<b>1000</b>	<b>2000 First</b>

Entered by

Date: 25.07.2011

Registrar / Controller of Examination