



INDIAN INSTITUTE OF INTEGRATED SCIENCE AND TECHNOLOGY

An Independent Institute Established by Trust Regd. Under Govt. India

RESULT CUM - DETAILED MARKS CARD

Name of the Candidate	V PARAMAIYAN	Father's Name	N VENKATACHALAM
Programme	B.TECH IN ELECTRONICS AND TELE-COMMUNICATION ENGINEERING	D.O.B	22.02.1987
Semester / Year	CONSOLIDATED- I	Session	2010-2013
Centre Code	TNC200050	Roll No.	16Q6018BTETE2010

Subject Code	Subject	Marks obtained	Minimum Pass Marks	Maximum Marks
SEM-III				
EDC	Engineering Mathematics – III	69	50	100
DCR	Analog Electronic Circuits	70	50	100
NWA	Logic Design	71	50	100
LCS	Network Analysis	73	50	100
VRC	Electronic Instrumentation	80	50	100
EFE	Field Theory	77	50	100
SEM-IV				
PDT	Engineering Mathematics – IV	69	50	100
SAS	Microcontrollers	78	50	100
EDC	Control Systems	74	50	100
EMD	Signals & Systems	77	50	100
ETP	Fundamentals of HDL	72	50	100
ICT	Linear ICs & Applications	76	50	100
SEM-V				
EME	Management and Entrepreneurship	66	50	100
ICA	Digital Signal Processing	69	50	100
MIP	Analog Communication	74	50	100
MMI	Information Theory & Coding	70	50	100
DCM	Fundamentals of CMOS	75	50	100
DSP	VLSI	79	50	100
TOTAL				
DIVISION				

CONTINUED IN CONSOLIDATED- II

Entered by

Date: 25.07.2013

Registrar / Controller of Examination



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Centre Code	TNC200050	Roll No.	16Q6018BTETE2010

Subject Code	Subject	Marks obtained	Minimum Pass Marks	Maximum Marks
SEM-VI				
DSD	Digital Communication	70	50	100
AWP	Microprocessors	76	50	100
FOC	Microelectronics Circuits	79	50	100
SMD	Antennas and Propagation	77	50	100
TLN	Operating Systems	71	50	100
VSL	Environmental Studies	80	50	100
SEM-VII				
EMS	Computer Communication Networks	77	50	100
MWE	Optical Fiber Communication	69	50	100
MMS	Power Electronics	70	50	100
WLC	Embedded System Design	79	50	100
WSN	Digital Switching Systems	72	50	100
IMP	Wireless Communication	70	50	100
SEM-VIII				
PRO	Project work	85	50	100
TOTAL DIVISION		2294	1550	3100 First

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