



# INDIAN INSTITUTE OF INTEGRATED SCIENCE AND TECHNOLOGY

An Independent Institute Established by Trust Regd. Under Govt. India

## RESULT CUM - DETAILED MARKS CARD

Name of the Candidate	AYITHEREDDY BHARATH KUMAR	Father's Name	AYITHEREDDY RAMU
Programme	B.TECH COMPUTER SCIENCE ENGG	D.O.B	10-05-1997
Semester / Year	CONSOLIDATED- I	Session	2014-2018
Centre Code	TNA210033	Roll No.	66A3BCSE3614

Subject Code	Subject	Marks obtained	Minimum Pass Marks	Maximum Marks
<b>SEM-I</b>				
EN-I	English - I	75	50	100
EM-I	Engineering Mathematics – I	85	50	100
EC-I	Engineering Chemistry - I	71	50	100
EP-I	Engineering Physics - I	81	50	100
CIT	Computers and Information Technology	70	50	100
CDS	C & Data Structures	77	50	100
PHL	Physics Laboratory	81	50	100
CHL	Chemistry Laboratory	82	50	100
EPL	Engineering Practices Laboratory	80	50	100
<b>SEM-II</b>				
EN-II	English - II	82	50	100
EM-II	Engineering Mathematics – II	89	50	100
EC-II	Engineering Chemistry - II	73	50	100
EP-II	Engineering Physics - II	77	50	100
NWT	Network Theory	83	50	100
SDC	Semiconductor Devices and circuits	70	50	100
DGL	Digital Laboratory	83	50	100
PGL	Programming Laboratory	78	50	100
<b>SEM-III</b>				
EM-III	Engineering Mathematics – III	81	50	100
DSG	Discrete Structures and Graph Theory	82	50	100
DPF	Data Processing and File Organization	78	50	100
LDI	Linear and Digital ICs Applications	77	50	100
<b>TOTAL</b>				
<b>DIVISION</b>				

CONTINUED IN CONSOLIDATED- II

Entered by

Date: 26.07.2018

Registrar / Controller of Examination



# INDIAN INSTITUTE OF INTEGRATED SCIENCE AND TECHNOLOGY

An Independent Institute Established by Trust Regd. Under Govt. India

## RESULT CUM - DETAILED MARKS CARD

Name of the Candidate	AYITHEREDDY BHARATH KUMAR	Father's Name	AYITHEREDDY RAMU
Programme	B.TECH COMPUTER SCIENCE ENGG	D.O.B	10-05-1997
Semester / Year	CONSOLIDATED- II	Session	2014-2018
Centre Code	TNA210033	Roll No.	66A3BCSE3614

Subject Code	Subject	Marks obtained	Minimum Pass Marks	Maximum Marks
STL	Switching theory and logic design	79	50	100
MEA	Managerial Economics and Accountancy	70	50	100
DSL	Data Structures Lab	80	50	100
CPL	Computer Practices Laboratory	84	50	100
<b>SEM-IV</b>				
PAS	Probability and Statistics	77	50	100
ETC	Electrical Technology	76	50	100
CPO	Computer Organization	79	50	100
DAA	Design and Analysis of Algorithms	66	50	100
OSY	Operating Systems	71	50	100
OOP	Object Oriented Programming	72	50	100
JIL	Java and Internet Programming Lab	85	50	100
OSL	Operating Systems Lab	87	50	100
<b>SEM-V</b>				
ITM	Interfacing through Microprocessors	79	50	100
OPR	Operations Research	76	50	100
DCM	Data Communications	86	50	100
TOC	Theory of Computation	87	50	100
PPL	Principles of Programming Languages	74	50	100
SPR	System Programming	70	50	100
CNL	Communications and Networks Lab	80	50	100
MPL	Microprocessors Lab	86	50	100
TOTAL DIVISION				

CONTINUED IN CONSOLIDATED- III

Entered by

Date: 26.07.2018

Registrar / Controller of Examination